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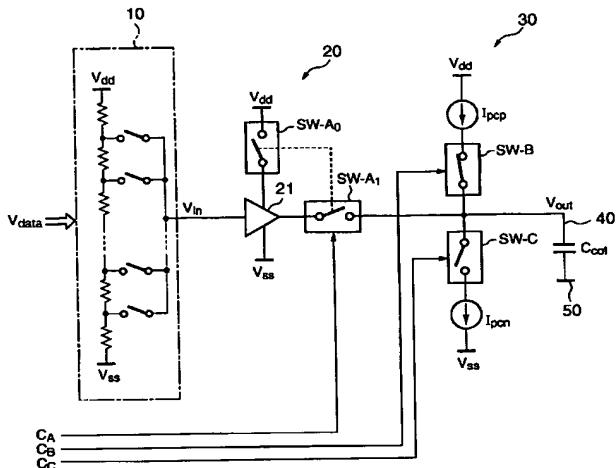
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**(54) Title: DISPLAY DEVICE DRIVING CIRCUIT**



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**(57) Abstract:** The present invention provides a driving circuit capable of reducing power consumption in an amplifier for outputting a target voltage. A driving circuit for driving a capacitive load  $C_{col}$  of a display device, comprising: driving signal supplying means (10) for supplying a driving signal  $V_{in}$  having a target voltage to be applied; an amplifying stage (20) for receiving the driving signal  $V_{in}$  and selectively outputting the driving signal  $V_{in}$  to the capacitive load  $C_{col}$ ; and a pair of current sources  $I_{pcp}$ ,  $I_{pcn}$  for selectively supplying a positive current and a negative current to the capacitive load  $C_{col}$ , respectively during their on-states. The driving circuit repeats a repetitive operation including a pre-operation where any one of the current sources  $I_{pcp}$ ,  $I_{pcn}$  is switched ON in accordance with the driving signal  $V_{in}$  and then switched OFF and a post-operation where the amplifying stage (20) is switched to a state for outputting the driving signal  $V_{in}$  to the capacitive load  $C_{col}$  after the pre-operation.



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